Cyclone Warning

Standalone Programming or Production Programming + Interactive Development

With its USB, Ethernet and serial interfaces the Cyclone Max provides fast communication to the PC. The Cyclone Max is particularly versatile and can be used in one of three modes.

**True Standalone Programming**
Load a memory image and a programming algorithm into the Cyclone from the PC and then you can disconnect the PC. The Cyclone Max will program on-chip and off-chip flash memory on your boards without requiring a PC – just plug the BDM in, push the buttons and it’s done. With 3Mbytes of internal memory and the ability to add a flash card there is plenty of space.

**Production Programming**
Link the Cyclone Max to a PC (whichever interface is the most convenient), then just download the memory image and algorithm once. From then on you can repeatedly command the super fast Cyclone to program and verify your boards at top speed. An optional package will allow multiple Cyclone Maxes to be controlled from a single computer.

**Interactive Debugging**
In addition its fast PC links allow the user to interactively command programs to be loaded into the memory through the BDM interface. Then breakpoints, memory readout and single stepping can be performed by our ICD package or your compiler’s debug environment. You can even do this via a C API for complex testing requirements.

All for only £550

PPC 5xx, 8xx and ColdFire – all with one unit!

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**Upgraded Logic Analysers**

Link have released the new 5000 range of Logic Analysers. They interface faster (USB2), have longer buffers at slow speed and more channels at higher speed than the 4000 range – all for less money.

See our web site for detailed specification of these analysers that will record at up to 160 channels and 500Msamples/sec. Prices start at £1340.

Computer Solutions Ltd  www.computer-solutions.co.uk
Explorer 200 High speed USB 2 protocol Analyser

The USB bus is becoming increasingly popular as a way of connecting embedded systems to PCs. Up to now it has been prohibitively expensive to purchase an Analyser with which to debug either your hardware or your application.

However, help for your budget is at hand with the Ellisys Explorer and Tracker ranges of USB Analysers.

Both work in the same way, they are placed on the USB bus under investigation and log the data detected in a non-intrusive manner (just as a logic analyser or a scope probe does).

They themselves are connected to an Analysis Computer via a USB link (preferably USB 2) and run the Ellisys Visual USB software package that collects data and afterwards displays it to the user.

An optional Class Protocol Analyser displays details of the standard messages used by Classes such as Mass Storage, Printer, Audio, Video, Still Image, CDC, HID (mice, joystick) and Bluetooth. Users can adapt it to support their own protocols.

Tracker 110

Able to operate with the USB’s two lower speeds, this is an ideal development tool where the USB ‘LOW’ and ‘FULL’ speeds of 1.5 Mbits/sec and 12 Mbits/sec are to be used by the device under test. These cost only £550.00

Explorer 200

This can monitor LOW, FULL and HIGH speed USB2 signals at up to 480Mbits/sec. An External trigger is included and split transactions are supported. A high performance trigger option includes triggering on packet source, destination and data to start and stop collection and provide an external trigger signal.

The basic Explorer 200 is £1610.00

The Explorer Pro is a bundle that includes Explorer 200, SDK, Class Analysers and advanced trigger option at a bundle price of less than £3200.00.

Compact TCP/IP now with SNMP, Wireless Ethernet and more CPUs

As well as adding the 68HCS12, MSP430 and dsPIC to the long list of 8 and 16-bit chips that can run MicroNet, a version is now available to support the increasing number of single-chip ARM CPUs.

The new SNMP option is a memory-efficient implementation of SNMP v1 or SNMP v2 capable of running on processors with limited memory resources (8K Bytes on 8051).

The new Wireless Ethernet option supports the IEEE 802.11b protocol, allowing the TCP/IP stack to be integrated into Wi-Fi systems. The initial release supports the Intersil Prism 2.5 wireless MAC.